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Inventor Information for 10/735644

Inventor Name	City	State/Country
FUJIMOTO, JUN	TOKYO	JAPAN

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US 20020058445 A1	US-PGPUB	20020516		Compact electronic apparatus with units each having independent housing for circuit board and functional devices	439/717		Tamura, Tetsuya et al.
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US 20050089172 A1	US-PGPUB	20050428		Vocal print authentication system and vocal print authentication program	380/275		Fujimoto, Jun
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US 20050159213 A1	US-PGPUB	20050721		Award money payment system, award ticket issuing device and award ticket accounting device	463/25		Okada, Kazuo et al.
US 20050164780 A1	US-PGPUB	20050728		Award money payment system, award ticket issuing device and award ticket accounting device	463/25		Okada, Kazuo et al.
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US 20060211485 A1	US-PGPUB	20060921	20	Gaming control system	463/25	463/29	Fujimoto; Jun
US 4353600 A	USPAT	19821012		Non-linkage type load sensing valve	303/9.67		Uchihara; Tatsuo et al.
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US 4776017 A	USPAT	19881004		Dual-step sound pattern matching	704/244		Fujimoto; Junichiroh
US 4827519 A	USPAT	19890502		Voice recognition system using voice power patterns	704/250		Fujimoto; Junichiroh et al.
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US 5011711 A	USPAT	19910430		Method for post-treatment of electroplated steel sheets for soldering	427/334	205/197; 427/404; 427/416; 427/417	Kanda; Katsumi et al.
US 5033089 A	USPAT	19910716		Methods for forming reference voice patterns, and methods for comparing voice patterns	704/247		Fujimoto; Junichiroh et al.
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US 5347612 A	USPAT	19940913		Voice recognition system and method involving registered voice patterns formed from superposition of a plurality of other voice patterns	704/243	704/254	Fujimoto; Junichiroh et al.
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